

IMPORTANT DATES

October 27, 2025

Tutorial Proposal Submission Deadline (Extended)

November 3, 2025

Tutorials proposal acceptance notification

May 1, 2026

Submission of final tutorial slides

Call For Tutorials

CALL FOR TUTORIALS

The IEEE International Symposium on Circuits and Systems (ISCAS) is the main flagship conference of the IEEE Circuits and Systems Society and the world's premier forum for researchers in the active fields of theory, design and implementation of circuits and systems. ISCAS 2026 will be held in Shanghai, China, from May 24 to 27, 2026. ISCAS 2026 is driven by the theme "Circuits and Systems for Intelligent Society", catalyzing transformative innovation at the intersection of Circuits, Systems, and Artificial Intelligence to foster the development of a smart and sustainable society.

Participation in tutorials and technical meetings is an excellent opportunity for lifelong learning, and ISCAS 2026 is committed to supporting your professional development. Suitable tutorial topics range from emerging research areas to established techniques of practical and industrial relevance within the scope of the Circuits and Systems Society and particularly within the themes of ISCAS 2026.

Tutorial Proposals Submission Guidelines

Please submit tutorial proposals by the Google Form location online at <https://2026.ieee-iscas.org/call-for-tutorials>.

The proposal should include a two-page (maximum) description of the tutorial (see below for details) and one-page professional biographies of the tutorial speakers that highlight their qualifications and track records in the proposed subject area of the tutorial. The proposal needs to contain the following information:

- ◆ **Format of the Tutorial:** Full-day or Half-day.
- ◆ **Title and Abstract:** Propose a title and an abstract that allows conference attendees to quickly and accurately understand the subject matter of the tutorial.
- ◆ **Motivation and Focus:** Explain why your topic is important to the ISCAS community and what you hope the participants will learn by attending your tutorial. In this section, please provide a syllabus outlining the contents of the tutorial and the learning outcomes. This syllabus should be realistic, given the time available.
- ◆ **List of Speakers:** This is in addition to the tutorial organizers. Some tutorials may feature different speakers that should be advertised, but it is not necessary, nor a requirement. For instance, a full-day tutorial may have, at most, four different speakers, while a half-day tutorial could have up to two speakers. If you plan to have multiple speakers, please provide their names and affiliations and clearly describe the parts of the tutorial that they will cover.
- ◆ **Basic Structure of the Tutorial:** In this section, please describe your use of the time available. (e.g., morning lectures: fundamentals and case studies; afternoon panel discussion or brainstorming sessions, etc.).

Authors of each of the two most successful tutorials will be invited to write a corresponding overview paper in the Circuits and Systems Magazine or in the IEEE Transactions on Circuits and System – Part I: Regular Papers, or IEEE Transactions on Circuits and Systems II: Express Briefs (TCAS-II), or IEEE Transactions on Biomedical Circuits and Systems (TBioCAS).